

COURSE: POWER ELECTRONICS CONVERTERS OPTIMIZATION		
MASTER: ELECTRONIC SYSTEMS ENGINEERING AND APPLICATIONS	YEAR: 2014-15	TERM: 1st

			w	EEKLY PLA	ANNING				
¥	SES	DATE		GRC (ma	DUPS rk X)	WEEKLY PROGRAMMING FOR STUDENT			
EEK	SION		DESCRIPTION		SEMINARS/ LAB ¹	DESCRIPTION	CLASS HOURS	HOMEWORK HOURS (Max. 7h week)	
9	1	18/11/2014	 1. Fundamentals 1.1 Fundamentals of switching dc-dc converters 	x			1,5		
9	2	20/11/2014	 1.2 Swithching proccess: harmonics and frequency effects in power semicunductors and passive componnets 1.2.1 Hard switching 1.2.2 Soft switching : ZCS and ZVS 1.2.3 Resonant conversion 1.2.4 Frequency effects summary 1.3 Optimization drivers: efficiency, power density, size, weight and cost 	x			1,5		

			w	EEKLY PLA	ANNING			
¥	SES			GROUPS (mark X)		WEEKLY PROGRAMMING FOR STUDENT		
		DATE	DESCRIPTION		SEMINARS/ LAB ¹	DESCRIPTION	CLASS HOURS	HOMEWORK HOURS (Max. 7h week)
9	3*	21/11/2014	 2. Selection of power semiconductors 2.1 Power diodes 2.2 Power MOSFETS * Making up missed class 06/01/2015 	х			1,5	
10	4	25/11/2014	2.3 IGBT 2.4 SiC and GaN 2.5 Application map	х			1,5	
10	5	27/11/2014	 3. Capacitors for power electronics 3.1 Fundamentals 3.2 Suitable Technologies for different fields of application 3.3 Ultracapacitors 	х			1,5	
11	6	02/12/2014	 4. Magnetic components design for low, medium and high frequency applications 4.1 Fundamentals 4.2 Magnetic laws in inductors and transformers 4.3 Core losses and winding losses 4.4 Core material and conductor type selection 	x			1,5	
11	7	04/12/2014	4.5 High frequency magnetics: Ferrite, Iron Powder, amorphous and nanocrystalline4.5.1 Case study 1: DC and AC inductors design	х			1,5	
12	8	09/12/2014	4.5.2 Case study 2:transformer design	х			1,5	
12	9	11/12/2014	 4.6 Low frequency magnetics: Laminated sillicon steel 4.6.1 Case study 3: DC and AC inductors design 4.6.2 Case study 2:transformer design 	х			1,5	

			W	EEKLY PLA	NNING				
WEEK	SESSION	DATE	DESCRIPTION	GRC (ma	DUPS rk X)	WEEKLY PROGRAMMING FOR S	TUDENT	JDENT	
13	10	16/12/2014	 5.1 Influence of the main component of the losses on the efficiency characteristic 5.2.1 Possibilities of minimizing semiconductor losses 5.2.2 Possibilities of minimizing passive component losses 	х			1,5		
13	11	18/12/2014	5.2 Multiphase approach and optimal partitioning of total power5.3 Topology selection	х			1,5		
141208/01/20155.4 Economical aspects 5.5 Optimization techniques		х			1,5				
15	13	13/01/2015	6. Industrial realization of the power converter4.1 Thermal management4.2 Protection devices and techniques	х			1,5		
151413/01/20154.3 MOSFET and IGBT drivers 4.4 Mechanical enclosures and IP code		х			1,5				
			¹ A maximum of 1-2 lab sessions			Subtotal 1	21	34	
Total 1 (Hours of class plus student homework hours between weeks 1-7)					mework h	ours between weeks 1-7)	55		

1-7	Tutorials, handing in, etc							10	
8	Assessment							3	7
							Subtotal 2	3	17
		Total 2 (Hours of c	class plus	s student h	nomework h	ours at week 8)		2	0

TOTAL (<i>Total 1 + Total 2</i>) 75
