uc3m Universidad Carlos III de Madrid

Vicerrectorado de Estudios Apoyo a la docencia y gestión del grado

COURSE: Computer Technology		
DEGREE:Computer Science and Engineering	YEAR: 1st	TERM: 2nd

WEEKLY PLANNING								
	S		TEACHING (mark X)		SPECIAL ROOM	WEEKLY PROGRAMMING FOR STUDENT		
W E E K	E S I O N	DESCRIPTION	L E C T U R E S	S E M I N A R S	FOR SESSION (Computer class room, audio-visual class room)	DESCRIPTION	CLASS HOURS (1,66=50+50 min)	HOMEWORK HOURS (Max. Estim. 6,5h)
1	1	Introduction. Information representation in digital systems	х			Get necessary material for the course (tools, etc.)	1,66	6,5
	2	Boolean Algebra and logic gates		Х		Study current lessons	1,66	
2	3	Introduction to VHDL design	Х			Study current lessons	1,66	6,5
	4	Exercises		Х		Exercises	1,66	0,3
3	5	Combinational circuits	Х			Study current lessons	1,66	6,5
3	6	Design of combinational circuits in VHDL. Examples		Х		Study current lessons	1,66	0,5
4	7	Exercises of combinational circuits	Х			Exercises	1,66	6,5
4	8	Exercises		Х		Exercises	1,66	0,3
5	9	Arithmetic circuits	Х			Study current lessons	1,66	6,5
3	10	Lab Practice 1		Х	Lab	Design and develop the proposed circuit	1,66	
6	11	Exercises	Х			Exercises	1,66	6,5
ь	12	Lab Practice 2		Х	Lab	Design and develop the proposed circuit	1,66	
7	13	Flip-flops	Х			Study current lessons	1,66	6,5
	14	Exercises		Х		Exercises and review lessons for the exam	1,66	
Q	15	Control I	х			Exercises and review lessons for the exam	1,66	65

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W E E K			L E C T U R E S	S E M I N A R S	SPECIAL ROOM FOR SESSION (Computer class room, audio-visual class room)	DESCRIPTION	CLASS HOURS (1,66=50+50 min)	HOMEWORK HOURS (Max. Estim. 6,5h)	
8	16	Exercises		Х		Exercises	1,66	0,5	
9	17	Registers and counters	Х			Study current lessons	1,66	6,5	
9	18	Sequential circuits in VHDL. Examples		Х		Study current lessons	1,66	5,5	
10	19	Finite State Machines	Х			Study current lessons	1,66	6,5	
10	20	Exercises		Х		Exercises	1,66	0,5	
11	21	Exercises	Х			Exercises	1,66	6,5	
	22	Lab Practice 3		Х	Lab	Design and develop the proposed circuit	1,66		
12	23	Memories	Х			Study current lessons	1,66	6,5	
		Exercises		Х		Exercises	1,66		
13		Introduction to digital systems	Х			Study current lessons	1,66	6,5	
	26	Lab Practice 4		Х	Lab	Design and develop the proposed circuit	1,66		
14	27	Exercises	Х			Exercises	1,66	6,5	
	28	Exercises		Х		Exercises and review lessons for the exam	1,66		
	29	Control II		Х		Exercises and review lessons for the exam	1,66	3,25	
	Subtotal 1							94	
	Total 1 (Hours of class plus student homework)							142	
15		Tutorials, handing in, etc		l	I	Submit design exercises and lab work	3,6		
16		ratorially namaling my etc				Prepare for final exam	3,0	-	
17 18		Assessment				Trepare for final exam	4	10	
	Subtotal 2						8	10	
	Total 2 (Hours of class plus student homework)						1	8	

TOTAL (<u>Maximun 160 horas</u>)

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