

COURSE: Embedded systems
DEGREE: Master's Degree in Electronics Systems Engineering

		WEEKL	Y PLANNING		
Week Session	Description	Location	WEEKLY PROGRAMMING FOR STUDENT		
			DESCRIPTION	CLASS HOURS	HOMEWORK HOURS (Mx. 7h week)
1 1	Presentation of the subject. 1 Introduction.	Classroom	Getting necessary material.	1,5	4
2	·	Classroom	Studying the given lessor	1,5	
2 3		Classroom	Studying the given lessor	1,5	5
4	4.1Introduction to SoPC. 4.2 Embedded uP (Xilinx)	Classroom	Studying the given lessor	1,5	
5	4.3 HW system design (xilinx)	Computer room	Reinforce the knowledge of the tool used in class	1,5	7
6	4.3(cont.) IP	Computer room	Reinforce the knowledge of the tool used in class. Submit the exercised made in class	1,5	
4 7 8	4.4 SW system design (Xilinx, SDK)	Computer room	Reinforce the knowledge of the tool used in class	1,5	7
	4.4 cont. (standalone, drivers and libraries)	Computer room	Reinforce the knowledge of the tool used in class	1.5	
5 9		Lab.	Reinforce the knowledge of the tool used in class. Prepare practice	1,5	7
10	Lab exercise 1	Lab.	Reinforce the knowledge of the tool used in class. Prepare practices	1,5	
6 11 12	Lab exercise 2	Lab.	Prepare practices	1,5	7
	4.4 cont. (OS)	Classroom	Studying the given lessor	1,5	
13	4.4 cont. (OS)	Computer room	Studying the given lesson. Submit the lab report	1,5	
14	5. Resources optimization	Classroom	Studying the given lessor	1,5	
				21	
15	Assessment	Classroom		3	7
	1 2 3 4 5 6 7 8 9 10 11 12 13	Presentation of the subject. 1 Introduction. 2 HW component 3 SW component 4.1Introduction to SoPC. 4.2 Embedded uP (Xilinx) 5 4.3 HW system design (xilinx) 6 4.3(cont.) IP 7 4.4 SW system design (Xilinx, SDK) 8 4.4 cont. (standalone, drivers and libraries) 9 4.5. Debugging 10 Lab exercise 1 11 Lab exercise 2 12 4.4 cont. (OS) 13 4.4 cont. (OS) 14 5. Resources optimization	SessionDescriptionLocation1Presentation of the subject. 1 Introduction.Classroom22 HW componentClassroom33 SW componentClassroom44.1Introduction to SoPC. 4.2 Embedded uP (Xilinx)Classroom54.3 HW system design (xilinx)Computer room64.3(cont.) IPComputer room74.4 SW system design (Xilinx, SDK)Computer room84.4 cont. (standalone, drivers and libraries)Computer room94.5. DebuggingLab.10Lab exercise 1Lab.11Lab exercise 2Lab.124.4 cont. (OS)Classroom134.4 cont. (OS)Computer room145. Resources optimizatiorClassroom	Presentation of the subject. 1 Introduction. Classroom Studying the given lessor Studying the given less	Description Description