Universidad Carlos III de Madrid

SUBJECT: Computer Technology	**YEAR: 1st**
DEGREE: Dual Bachelor: Computer Science Engineering - Business Administration	**SEMESTER: 2nd**

WEEKLY PLANNING								
Week	**Session**	**Description**	**Group**		**Location**	**Weekly programming for student**		
			Big	Small		Description	Class hours	Homework hours
1	1	Introduction. Boole Algebra	X		Classroom	Study developed contents	1,67	6
	2	Logic gates and logic function implementation	X		Classroom	Study developed contents	1,67	
	3	Information representation in digital systems		X	Classroom	Study developed contents	1,67	
2	4	Combinational circuits	X		Classroom	Study developed contents	1,67	5
	5	Exercises about number systems		X	Classroom	Exercises	1,67	
3	6	Binary arithmetics	X		Classroom	Study developed contents	1,67	6
	7	Exercises about funciton logic implementation and combinational circuits		X	Classroom	Exercises	1,67	
4	8	Ejercicios	X		Classroom	Study developed contents	1,67	6
	9	**Lab. Session 1**		X	Lab.	Exercises, lab. session preparation	1,67	
5	10	Flip-flops	X		Classroom	Study developed contents	1,67	6
	11	Exercises about combinational circuits		X	Classroom	Exercises	1,67	
6	12	**Control 1**	X		Classroom	Control preparation	1,67	8
	13	(Review Control 1) Exercises about flip-flops and chronograms		X	Classroom	Exercises	1,67	
7	14	Synchronous sequential circuits (I)	X		Classroom	Study developed contents	1,67	6
	15	**Lab. Session 2**		X	Lab.	Exercises, lab. session preparation	1,67	
8	16	Synchronous sequential circuits (II)	X		Classroom	Study developed contents	1,67	6
	17	Exercises about flip-flops and finite state machines		X	Classroom	Exercises	1,67	
9	18	Registers and counters	X		Classroom	Study developed contents	1,67	6
	19	Exercises about finite state machines		X	Classroom	Exercises	1,67	
10	20	Exercises about registers and counters	X		Classroom	Study developed contents	1,67	4
	21	**Lab. Session 3**		X	Lab.	Exercises, lab. session preparation	1,67	
11	22	Memories	X		Classroom	Study developed contents	1,67	5
	23	Exercises about memories		X	Classroom	Exercises	1,67	
12	24	**Control 2**	X		Classroom	Control preparation	1,67	8
	25	(Review Control 2) Exercises about memories		X	Classroom	Exercises	1,67	
13	26	Programmable logic devices	X		Classroom	Study developed contents	1,67	6
	27	**Lab. Session 4**		X	Lab.	Exercises, lab. session preparation	1,67	
14	28	Introduction to digital systems and microprocessors	X		Classroom	Study developed contents	1,67	5
	29	Exercises about memories and programmable logic devices		X	Classroom	Exercises	1,67	
SUBTOTAL		0					48,43	83
15		0						
16-18		0			Classroom		3	15
TOTAL							149,43	