

Week	Session	Description	Location
1	1	Presentation of the subject. 1.- Introduction.	Classroom
	2	2.- HW component	Classroom
2	3	3.- SW component	Classroom
	4	4.1.-Introduction to SoPC. 4.2.- Embedded uP (Xilinx	Classroom
3	5	4.3.- HW system design (xilinx), IP integration (unc existente), ILA	Computer room
	6	4.4.- SW system design (Xilinx, SDK), standalone, drivers	Computer room
4	7	4.5. Debugging (FPGA lending)--> HW	Lab.
	8	4.5. Debugging (FPGA lending)--> SW	Lab.
5	9	4.5. Debugging (FPGA lending)--> exercis	Lab.
	10	Lab exercise 1	Lab.
6	11	4.4.- SW system design (OS) (I,	Computer room
	12	4.4.- SW system design (OS) (II)	Lab.
7	13	Lab exercise 2	Lab.
	14	5. Optimization	Classroom