

Chronogram: Electronic Technology in Biomedicine

WEEK	DESCRIPTION	LECTURES (g.48-49) 7.1.J06 Tuesday 15:00-17:00	SEMINARS (g.48-49) 7.1.J06/J08 Friday 17:00-19:00
W1	T1. Circuit Theory (I) 1. Ohm law. 2. Kirchhoff laws 3. Current and voltage sources. 4. Drawing electrical schematics.	P1 (29/01)	
	T1. Circuit Theory (I): Exercises 1. Ohm law. 2. Kirchhoff law. 3. Ideal current and voltage sources. 4. Drawing electrical schematics.		P2/P3 (1/02)

W2	T1. Circuit Theory (II) 4. Superposition theorem. 5. Thevenin and Norton theorem. 6. Real voltage and current sources.	P1 (5/02)	
	T1. Circuit Theory (II): Exercises 4. Superposition theorem. 5. Thevenin and Norton theorem. 6. Real voltage and current sources.		P2/P3 (8/02)
W3	T1. Circuit Theory (III) 7. Capacitors and Inductors (C and L). 8. Time response of C and L. 9. Universal equation for C and L.	P1 (12/02)	
	T1. Circuit Theory (III): Exercises 7. Capacitors and Inductors (C and L). 8. Time response of C and L. 9. Universal equation for C and L.		P2/P3 (15/02)
W4	T1. Computer circuit simulation I 1. Introduction to computer simulation. 2. DC analysis 3. Transient analysis	P1 (19/02)	
	Lab 1: Electronic components (I) G48: 1.1.F08, G49: 1.1.F05		P1-P4 (22/02)

W5	T1. Circuit Theory (IV) 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.	P1 (26/02)	
	T1. Circuit Theory (IV): Exercises 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.		P2/P3 (1/03)
W6	T1. Computer circuit simulation II 1. AC analysis 2. Controlled sources and two port networks 3. Computer transfer function evaluation.	P1 (05/03)	
	Inf 1: Computer room exercise G.48: Aula INF 2.2.C.03 DUAL G.49:INF2.2.C.04DUAL:		P2/P3 (08/03)
W7	Partial Exam I (Circuit Theory)	P1 (12/03)	
	Lab 2: Electronic components (II) G48: 1.1.F08, G49: 1.1F05		P1-P4 (15/03)

W5	T1. Circuit Theory (IV) 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.	P1 (26/02)	
	T1. Circuit Theory (IV): Exercises 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.		P2/P3 (1/03)
W6	T1. Computer circuit simulation II 1. AC analysis 2. Controlled sources and two port networks 3. Computer transfer function evaluation.	P1 (05/03)	
	Inf 1: Computer room exercise G.48: Aula INF 2.2.C.03 DUAL G.49:INF2.2.C.04DUAL:		P2/P3 (08/03)
W7	Partial Exam I (Circuit Theory)	P1 (12/03)	
	Lab 2: Electronic components (II) G48: 1.1.F08, G49: 1.1F05		P1-P4 (15/03)

W5	T1. Circuit Theory (IV) 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.	P1 (26/02)	
	T1. Circuit Theory (IV): Exercises 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.		P2/P3 (1/03)
W6	T1. Computer circuit simulation II 1. AC analysis 2. Controlled sources and two port networks 3. Computer transfer function evaluation.	P1 (05/03)	
	Inf 1: Computer room exercise G.48: Aula INF 2.2.C.03 DUAL G.49:INF2.2.C.04DUAL:		P2/P3 (08/03)
W7	Partial Exam I (Circuit Theory)	P1 (12/03)	
	Lab 2: Electronic components (II) G48: 1.1.F08, G49: 1.1F05		P1-P4 (15/03)

W5	T1. Circuit Theory (IV) 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.	P1 (26/02)	
	T1. Circuit Theory (IV): Exercises 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.		P2 (1
W6	T1. Computer circuit simulation II 1. AC analysis 2. Controlled sources and two port networks 3. Computer transfer function evaluation.	P1 (05/03)	
	Inf 1: Computer room exercise G.48: Aula INF 2.2.C.03 DUAL G.49:INF2.2.C.04DUAL:		P2 (08
W7	Partial Exam I (Circuit Theory)	P1 (12/03)	
	Lab 2: Electronic components (II) G48: 1.1.F08, G49: 1.1F05		P1 (15

W5	T1. Circuit Theory (IV) 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.	P1 (26/02)	
	T1. Circuit Theory (IV): Exercises 10. DC and AC circuit analysis. 11. Frequency response of R, C and L circuits.		P2 (1
W6	T1. Computer circuit simulation II 1. AC analysis 2. Controlled sources and two port networks 3. Computer transfer function evaluation.	P1 (05/03)	
	Inf 1: Computer room exercise G.48: Aula INF 2.2.C.03 DUAL G.49:INF2.2.C.04DUAL:		P2 (08
W7	Partial Exam I (Circuit Theory)	P1 (12/03)	
	Lab 2: Electronic components (II) G48: 1.1.F08, G49: 1.1F05		P1 (15