

Academic Year: (2023 / 2024)

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Department assigned to the subject: Electronic Technology Department

Coordinating teacher: PATON ALVAREZ, SUSANA

Type: Electives ECTS Credits : 6.0

Year : Semester :

REQUIREMENTS (SUBJECTS THAT ARE ASSUMED TO BE KNOWN)

Linear Systems, Electronic Components and Circuits

LEARNING OUTCOMES

CB1. Students have demonstrated possession and understanding of knowledge in an area of study that builds on the foundation of general secondary education, and is usually at a level that, while relying on advanced textbooks, also includes some aspects that involve knowledge from the cutting edge of their field of study.

CB2. Students are able to apply their knowledge to their work or vocation in a professional manner and possess the competences usually demonstrated through the development and defence of arguments and problem solving within their field of study.

CB3. Students have the ability to gather and interpret relevant data (usually within their field of study) in order to make judgements which include reflection on relevant social, scientific or ethical issues.

CB4. Students should be able to communicate information, ideas, problems and solutions to both specialist and non-specialist audiences.

CB5. Students will have developed the learning skills necessary to undertake further study with a high degree of autonomy.

CG2. Learn new methods and technologies from basic scientific and technical knowledge, and being able to adapt to new situations.

CG3. Solve problems with initiative, decision making, creativity, and communicate and transmit knowledge, skills and abilities, understanding the ethical, social and professional responsibility of the engineering activity. Capacity for leadership, innovation and entrepreneurial spirit.

CG4. Solve mathematical, physical, chemical, biological and technological problems that may arise within the framework of the applications of quantum technologies, nanotechnology, biology, micro- and nano-electronics and photonics in various fields of engineering.

CG5. Use the theoretical and practical knowledge acquired in the definition, approach and resolution of problems in the framework of the exercise of their profession.

CG6. Develop new products and services based on the use and exploitation of new technologies related to physical engineering.

CG7. Undertake further specialized studies, both in physics and in the various branches of engineering.

CE13. Understand and handle solid state physical principles relevant to engineering and, in particular, semiconductors for application in electronic and photonic components, as well as the fundamentals and applications of analog and digital electronics and microprocessors.

CT1. Work in multidisciplinary and international teams as well as organize and plan work making the right decisions based on available information, gathering and interpreting relevant data to make judgments and critical thinking within the area of study.

RA1. To have acquired sufficient knowledge and proved a sufficiently deep comprehension of the basic principles, both theoretical and practical, and methodology of the more important fields in science and technology as to be able to work successfully in them.

RA2. To be able, using arguments, strategies and procedures developed by themselves, to apply their knowledge and abilities to the successful solution of complex technological problems that require creating and innovative thinking.

RA3. To be able to search for, collect and interpret relevant information and data to back up their conclusions including, whenever needed, the consideration of any social, scientific and ethical aspects relevant in their field of study.

RA4. To be able to successfully manage themselves in the complex situations that might arise in their academic or professional fields of study and that might require the development of novel approaches or solutions.

RA6. To be aware of their own shortcomings and formative needs in their field of specialty, and to be able to plan and organize their own training with a high degree of independence.

OBJECTIVES

The aim of this course is to provide the students with a solid knowledge in a number of key horizontal techniques in electronic systems. During the development of this subject, special emphasis will be placed on the application of these techniques to specific equipment and subsystems commonly used in telecommunications, both for signal processing as well as equipment power supply. To achieve this objective, students will acquire the following specific abilities:

- Understand the operation of electronic circuits with negative feedback and their frequency response
- Analyse and evaluate the most common oscillator circuits
- Understand the functioning of real operational amplifiers and their linear and nonlinear applications
- Understand the operation of the most commonly found electronic subsystems used in signal processing and communications such as timers, VCOs and PLLs
- Understand the operation and applications of power supplies and power equipment for telecommunication systems

In terms of general abilities or skills, the following areas will be worked upon throughout the development of the subject:

- Ability to work cooperatively in a team, knowing how to adapt the requirements and working conditions of the subsystem developed so that they operate correctly within a global system which is not only electronic. This aspect will be covered by means of the development of examples and case studies.
- Ability to identify, formulate and solve problems in Engineering
- Ability to use techniques and tools required in modern engineering to reduce the equipment development time

DESCRIPTION OF CONTENTS: PROGRAMME

BLOCK 1

- Electronic Feedback Circuits
 - o Basic concepts of the theory related to feedback electronics
 - o Electronic feedback circuit topologies
 - o Calculation of the gain, input impedance and output impedance in feedback circuits.
 - o Conception of the practical method used to solve negative feedback circuits. Example
 - o Basic configurations of the beta network according to the different topologies
 - o Study of feedback circuits for each one of the different topologies.
- Frequency Analysis of Electronic Feedback Circuits
 - o Frequency analysis of a feedback amplifier
 - o Stability study of a feedback amplifier using the Bode diagram
 - o Compensation methods. Exercises
- Oscillators
 - o General configuration of an oscillator. Start up condition and oscillator maintenance
 - o RC oscillators:
 - o Amplitude limiters
 - o LC Oscillators: Colpitts, Hartley and Clapp Oscillators. Crystal Oscillators (Xtal)

BLOCK 2

- Real Operational Amplifiers and their Applications
 - o Real operational amplifier characteristics
 - o Linear applications. Active filters as linear application
 - o Non-linear applications
- Electronic Subsystems for signal processing and communications: Integrated timers and applications. PLLs and Applications.
 - o The 555 integrated timer: monostable, astable and VCO modes

- o PLL:
 - Blocks diagram and working principle
 - PLL components: phase detectors, filters, VCOs
 - PLL transfer function. PLL types.
 - 1st and 2nd order PLL. Examples.
 - PLL Applications.

BLOCK 3

- Linear Voltage Regulators
 - o Series- shunt feedback in linear voltage regulators
 - o Basic design of a linear voltage regulator
 - o Power and efficiency calculations
- Switching Voltage Regulators
 - o Fundamentals of switching DC/DC Converters
 - o Basic operation and design of Buck converter
 - o Negative feedback in a switching DC/DC Converter
 - o DC/DC and AC/DC Converters for Telecommunications. SAls
- Energy Converters
 - o Basic analysis of a photovoltaic generator
 - o Description of other systems related to electrical energy generation

LEARNING ACTIVITIES AND METHODOLOGY

The teaching methodology will include::

- 14 Magisterial Classes, where the students will be presented with the basic knowledge they must acquire. Students will be provided with lecture notes and key reference texts, which will enable them to complete and acquire a more in depth knowledge of the subject.
 - 11 Problems Classes these are aimed at the solving of exercises and examples within the context of real case studies. These classes will be complemented with the resolution of practical exercises on behalf of the student, which in some cases may require the use of computer based simulation programs.
 - 4 Laboratory Practical Sessions, here the student will design, model and characterise electronic systems within the area of communications and real applications.
- Group tutorial session. At least, a group tutorial session will be carried out during the recovery week as revision and final exam preparation. (See the weekly schedule for additional details)

ASSESSMENT SYSTEM

% end-of-term-examination/test:	60
% of continuous assessment (assignments, laboratory, practicals...):	40

A continuous evaluation system will be carried out where the following will be evaluated:

1. Mandatory Laboratory Practical Sessions (20%)

The knowledge acquired by the student will be evaluated by means of the experimental implementation of several of the circuits analysed previously in the Magisterial and Problems classes. The laboratory practices will be carried out in groups and during them a complete electronic system will be developed. The system will be evaluated in a laboratory exam.

2. Problem and/or quiz solving for every thematic block (20%)

Solving of problems and/or quizzes proposed for each thematic block, to be resolved individually.

3. Mandatory Final exam (60%).

Also, at the end of the course a final exam will be carried out where the global knowledge acquired by the students will be evaluated. A minimum mark will be required (4.5 over 10 points) to fulfil the requirements of continuous evaluation process.

BASIC BIBLIOGRAPHY

- A. S. SEDRA, K.C. SMITH, T. C. CARUSONE, , V. GALDET Microelectronic Circuits, Oxford University Press, 8th edition

- D. JOHNS, K. MARTIN, T. C. CARUSONE Analog Integrated Circuits Design, John Wiley and Sons, 2nd edition
- M. H. RASHID Microelectronic Circuits: Analysis and Design, CL-Engineering, 2010
- N. MOHAN First Course on Power Electronics, MN Power Electronics (MNPERE), 2009