

Academic Year: (2021 / 2022)

Review date: 26-06-2021

Department assigned to the subject: Computer Science and Engineering Department

Coordinating teacher: FERNANDEZ MUÑOZ, JAVIER

Type: Electives ECTS Credits : 6.0

Year : 4 Semester :

REQUIREMENTS (SUBJECTS THAT ARE ASSUMED TO BE KNOWN)

Operating Systems Design (Course: 3 / Semester: 2)

Computer Architecture (Course: 3 / Semester: 1)

DESCRIPTION OF CONTENTS: PROGRAMME

Syllabus:

1. Introduction to Real-Time and Embedded Systems
2. Cyclic Systems and Multiprogrammed Systems
3. Cyclical Task Planning
4. Planning with Task Priorities
5. Design with Microprocessors of Embedded Architectures
6. Embedded and Real-Time System Design
7. Embedded and Real-Time Operating Systems
8. Dynamic Task Planning and Quality of Service

LEARNING ACTIVITIES AND METHODOLOGY

Theoretical lectures: 1 ECTS. The goal is for the student to obtain the cognitive specific competencies of the subject as well as the transversal competencies like analytical and abstraction skills.

Practical lectures: 1 ECTS. The goal is for the student to obtain the instrumental specific competencies of the subject as well as the transversal competencies like problem resolution and the application of knowledge.

Continuous evaluation exercises: 1,5 ECTS. Started at the practical lectures and finished the goal of these exercises is to complete the instrumental specific competencies and to initiate the attitudinal specific competencies as well as the transversal competencies like problem resolution and the application of knowledge.

Projects: 2 ECTS. Developed without the presence of the teacher, their goal is to complete and to integrate the achievement of all the specific and transversal competencies by developing two projects where is well documented the approach to the problem, the chosen method to solve it, the obtained results and the interpretation of them.

Office hours: Individual assistance (individual tutoring) or group assistance (group tutoring) for the students given by the teacher.

Final exam: 0,5 ECTS. The goal is to enhance and complete the development of the cognitive and procedural specific competencies. It reflects specifically the leverage of the theoretical lectures.

ASSESSMENT SYSTEM

The exercises and exams acts as a training activity, but also as a way of measurement for the evaluation system. The evaluation system includes the evaluation of both the teacher-directed academic activities and the projects following the next weighting. (The relation between the competencies and the teaching activities is not specified because these relation has been indicated before.)

Continuous Evaluation exercises: 10%

Final projects: 40%

Final Exam: 50%

Minimum grade on the exam: 4 (over 10)

Minimum grade on the projects: 3 (over 10) on each one

% end-of-term-examination: 50

% of continuous assessment (assignments, laboratory, practicals...): 50

BASIC BIBLIOGRAPHY

- Alan Burns and Andy Wellings Real-Time Systems and Programming Languages: Ada, Real-Time Java and C/Real-Time POSIX (4th Edition) , Pearson Education , 2009

ADDITIONAL BIBLIOGRAPHY

- Bill Gallmeister Posix, O'Reilly, 1995.
- Bradford Nichols, Dick Butlar, Jacqueline Farrell Pthreads programming, O'Reilly, 1996..
- Hermann Kopetz Real-Time Systems. Design Principles for Distributed Embedded Applications, Kluwer, 1997.
- J.P. Cohoon & J.W. Davidson The C Programming Language. 2nd. ed (ANSI-C), Prentice-Hall, 1991.
- John Barnes Programming in Ada 95, 2nd. ed. Addison-Wesley, 1998..